**Long Short-Term Memory (LSTM)**

SEMINAR REPORT

Submitted By

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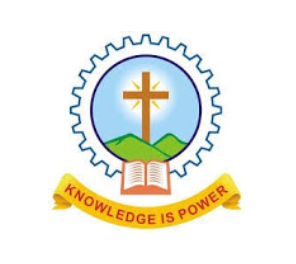
to

The APJ Abdul Kalam Technological University

In partial fulfillment of the requirements of the award of the Degree

of

Master of Computer Application



**Department of Computer Application**

Mar Athanasius College of Engineering

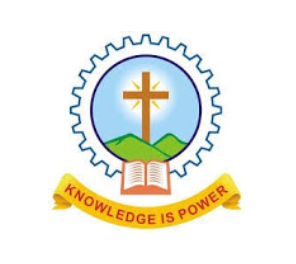
Kothamangalam, Kerala, India 686666

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**DEPARTMENT OF COMPUTER APPLICATION**

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**CERTIFICATE**

This is to certify that the seminar report entitled “Long Short-Term Memory (LSTM)” is a bonafide record of the work done by Anlin Albert (MAC20MCA2003) in the partial fulfillment of the requirements for the award of the Degree of MASTER OF COMPUTER APPLICATION in Computer Science and Engineering of APJ Abdul Kalam Technology University.

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**ABSTRACT**

Long Short-Term Memory is an advanced version of recurrent neural network (RNN) architecture that was designed to model chronological sequences and their long-range dependencies more precisely than conventional RNNs. The major highlights include the interior design of a basic LSTM cell, the variations brought into the LSTM architecture, and a few applications of LSTMs that are highly in demand such as fall detection, and human activity recognition.

LSTM has been so designed that the vanishing gradient problem is almost completely removed, while the training model is left unaltered. Long-time lags in certain problems are bridged using LSTMs which also handle noise, distributed representations, and continuous values. LSTMs provide us with a large range of parameters such as learning rates, and input and output biases. Hence, no need for fine adjustments.

The LSTM contains special units called memory blocks in the recurrent hidden layer. The memory blocks contain memory cells with self-connections storing the temporal state of the network in addition to special multiplicative units called gates to control the flow of information. Each memory block in the original architecture contained an input gate and an output gate. The input gate controls the flow of input activations into the memory cell. The output gate controls the output flow of cell activations into the rest of the network. Later, the forget gate was added to the memory block. This addressed a weakness of LSTM models preventing them from processing continuous input streams. The forget gate scales the internal state of the cell before adding it as input to the cell through the self-recurrent connection of the cell, therefore adaptively forgetting or resetting the cell’s memory. In addition, the modern LSTM architecture contains peephole connections from its internal cells to the gates in the same cell to learn the precise timing of the outputs